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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/055,991	01/28/2002	Hiroyuki Miwa	SON-1351/DIV	3294

TITLE OF INVENTION: METHOD OF MAKING A BIPOLAR TRANSISTOR HAVING A REDUCED BASE TRANSIT TIME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	03/31/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
GBREMARIAM, SAMUEL A	2811	438-348000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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**RADER, FISHMAN &
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SONY CORPORATION**TOKYO, JAPAN**Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature) **RONALD P. KANANEN** 24, 104**MARCH 31, 2004**

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